**TIME** | **TUESDAY 10th NOVEMBER** | **WEDNESDAY 11th NOVEMBER**
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08:45 | Registration | The Evolution of Commercial EMC Standards
09:30 | Session 1: Welcome and Keynote I | Keynote II
09:30 | Welcome to EMC Compo 2015 | President Suite (Presentation room)
09:45 | Maxwell’s Legacy – The Heart and Soul of the EMC Discipline | President Suite (Presentation room)
10:30 | Coffee Break | Preliminary Study of Automatic Control Gain Loop Subjected to Pulse-modulated Radiofrequency Interference
11:00 | Session 2: Design for EMC I | Immunity I
11:22 | DC/DC Converter Dead-Time Variation Analysis and Far-Field Radiation Estimation | Immunity I
11:44 | A Highly-Digitized Automotive CAN Transceiver in 0.14-μm High-Voltage SOI CMOS | Immunity I
12:06 | EMC and Switching Loss Improvement for Fast Switching Power Stages by dv/dt Optimization with 10ns Variable Current Source Gate Driver | Immunity I
12:30 | Lunch Break | Functional analysis of an integrated communication interface during ESD
13:30 | Session 3: Measurement Techniques I | Near-Field Injection on a Safe System Basis Chip at Silicon Level
13:52 | Electromagnetic Coupling Circuit Model of a Magnetic Near-Field Probe to a Microchip Line | Near-Field Injection on a Safe System Basis Chip at Silicon Level
14:14 | Bandgap Failure Study Due To Parasitic Bipolar Substrate Coupling In Smart Power Mixed ICs | Experimental Validation of the Generalized Accurate Modelling Method for System-Level Bulk Current Injection Setups up to 1 GHz
14:36 | Fundamental Study on Randomized Processing in Cryptographic IC Using Variable Clock Against Correlation Power Analysis | Relation between Internal Terminal Voltage and Immunity Behavior of LDO Regulator Circuits
15:00 | Coffee Break | Radiated Electromagnetic Immunity Analysis of VCO Using IC Stripline Method
15:30 | Session 4: Design for EMC II (Poster) | Analysis of On-Chip Digital Noise Coupling Path for Wireless Communication IC Test Chip
15:30 | An EMi robust LfH Driver with low Electromagnetic Emission | Characterization of the Immunity of Integrated Circuits (ICs) at Waveler Level
15:30 | On-Chip Watchdog to Monitor RTOS Activity in MPSoC Exposed to Noisy Environment | Time-domain EMI Measurement Methodology
15:30 | Introducing of multilayer Buck converters in automotive analog designs to reduce EME | Time-domain EMI Measurement Methodology
15:30 | Resonance Analysis for EMC Improvement in Integrated Circuits | Time-domain EMI Measurement Methodology
15:30 | Role of IC Substrate and ESD Protections in Noise Propagation: Design and Modelling of Dedicated Test Chip in 40 nm Technology | Time-domain EMI Measurement Methodology
15:30 | Improving the Shielding Effectiveness of a Board-Level Shield by Bonding it with the Waveguide-Below -Cutoff Principle | Time-domain EMI Measurement Methodology
15:30 | Analysis of EMI Reduction Methods of DC-DC Buck Converter | Time-domain EMI Measurement Methodology
15:30 | Methodology for interference analysis during early design stages of high-performance mixed-signal ICs | Time-domain EMI Measurement Methodology
15:30 | Dynamic Multi-Parameter Response Model for SEED Analysis | Time-domain EMI Measurement Methodology
15:30 | Arbitrary Shape Multilayer Interconnects EMC Modelling and Optimization | Time-domain EMI Measurement Methodology
15:30 | EMC I (Poster) | Time-domain EMI Measurement Methodology
15:30 | ESD test at component level | Time-domain EMI Measurement Methodology
18:00 | Evening Session | Welcome Reception
19:00 | Close | Evening Session
22:00 | Close | Evening Session
22:00 | Close | Visit to James Clerk Maxwell Foundation
**THURSDAY 12th NOVEMBER**

09:00  | **Session W2:** System decoupling capacitor selection and placement to minimize radiation and immunity issues (Part I)  

09:15  | **Introduction**  

09:30  | **Presidents Suite (Presentation room)**  

09:45  | **Future needs in EMC of ICs - Are you hearing the voice of industry?**  
Etienne Sicard — INSA  

10:00  | **Presidents Suite (Presentation room)**  

10:30  | **Coffee Break**

11:00  | **Session 9:** Keynote III  
**Presidents Suite (Presentation room)**  

11:22  | **Simulation of Radiated Emission during the Design Phase based on Scattering Parameter Measurement**  
Herbert Hardl, Gunther Winkler and Bernd Deutschmann  

11:44  | **Large domain validity of MOSFET Microwave Rectification Response**  
Clevis Pucur, Jeremy Nassat and Patrick Hoffmann  

12:06  | **EMC performance analysis of a Processor/Memory System using PCB and Package-On-Package**  
Etienne Sicard, Alexandre Boyer, Pracilia Fernandes Lopez, An Zhou, Nicolas Marier and Frederic Lafon

12:30  | **Lunch Break**

13:30  | **Coffee Break**

13:52  | **Session 10:** Modelling I  
**Presidents Suite (Presentation room)**

14:22  | **Radiation Characteristics of Small Loop Antenna above Perforated Finite Image Plane**  
Raul Breic, Renaud Gilin, Bart Nauwelaers and Adrijan Baric

14:44  | **Smart Power IC Macromodeling for DPI**  
Analysis  
Hamid Herrmann, Wolfgang Horchler, Stefan Schweda, Philipp Schreiber, Frank Körö, Marco Bigronne and Francesco Fiori

15:06  | **A methodologic project to characterize and model EMC behavior of COTS components after ageing**  
André Durier

15:30  | **Coffee Break**

15:52  | **Session W1:** System decoupling capacitor selection and placement to minimize radiation and immunity issues (Part II)  
**Presidents Suite (Poster area)**

16:44  | **A methodologic project to characterize and model EMC behavior of COTS components after ageing**  
André Durier

17:30  | **EMC Compo Gala Dinner at Ghillie Dhu**

00:00  | **Close**

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**FRIDAY 13th NOVEMBER**

09:00  |  

09:15  | **The power delivery system**  
Kristoffer Skytte

10:00  | **Presidents Suite (Presentation room)**

10:30  | **Simple last board measurements and results**  
John Phillips

11:00  | **Session 11:** ANSYS Workshop  
1925 Suite  

11:52  | **A methodologic project to characterize and model EMC behavior of COTS components after ageing**  
André Durier

12:06  | **Summary and further discussion**  
Kristoffer Skytte

12:30  | **Lunch Break**

13:30  | **Coffee Break**

13:52  | **Session 11:** ANSYS Workshop  
1925 Suite  

(Workshop)

14:00  | **Conference closing remarks**  
Kristoffer Skytte

14:30  | **Smart Power IC Macromodeling for DPI**  
Analysis  
Hamid Herrmann, Wolfgang Horchler, Stefan Schweda, Philipp Schreiber, Frank Körö, Marco Bigronne and Francesco Fiori

14:44  | **Shielding Structures for Through Silicon Via (TSV) to Active Circuit Noise Coupling in 3D IC**  
Jeam-Oui Lien, Manho Lee, Daniel H. Jung, Jonghoon Kim, Sunhm Choi, Hyunsuk Lee and Jungho Kim

15:06  | **Building Interchangeable Black-Box Models of Integrated Circuits for EMC Simulations**  
Markus Magat, Christian Stockreiter, Oliver Eisenberger, Ramazan Mitrochin and Adrijan Baric

15:30  | **Demonstration of optimization of the power delivery system on a large design**  
Kristoffer Skytte

15:44  | **Session 11:** ANSYS Workshop  
1925 Suite  

(Workshop)

16:44  | **A methodologic project to characterize and model EMC behavior of COTS components after ageing**  
André Durier

17:30  | **EMC Compo Gala Dinner at Ghillie Dhu**

00:00  | **Close**

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**EMC Compo 2015 Concludes**

**Murrayfield Rugby Stadium**  
Edinburgh, EH12 5PP

**James Clerk Maxwell Foundation**  
14 India Street, Edinburgh, EH3 6EZ

**James Clerk Maxwell Statue**  
George Street, Edinburgh

**Ghillie Dhu**  
2 Rutland Street, Edinburgh, EH1 2AD

**Edinburgh v Grenoble Rugby Match**, Murrayfield Rugby Stadium (Ticket required)